

or

"Simulation-based Code Duplication for Enhancing Compiler Optimizations"

David Leopoldseder
david.leopoldseder@jku.at

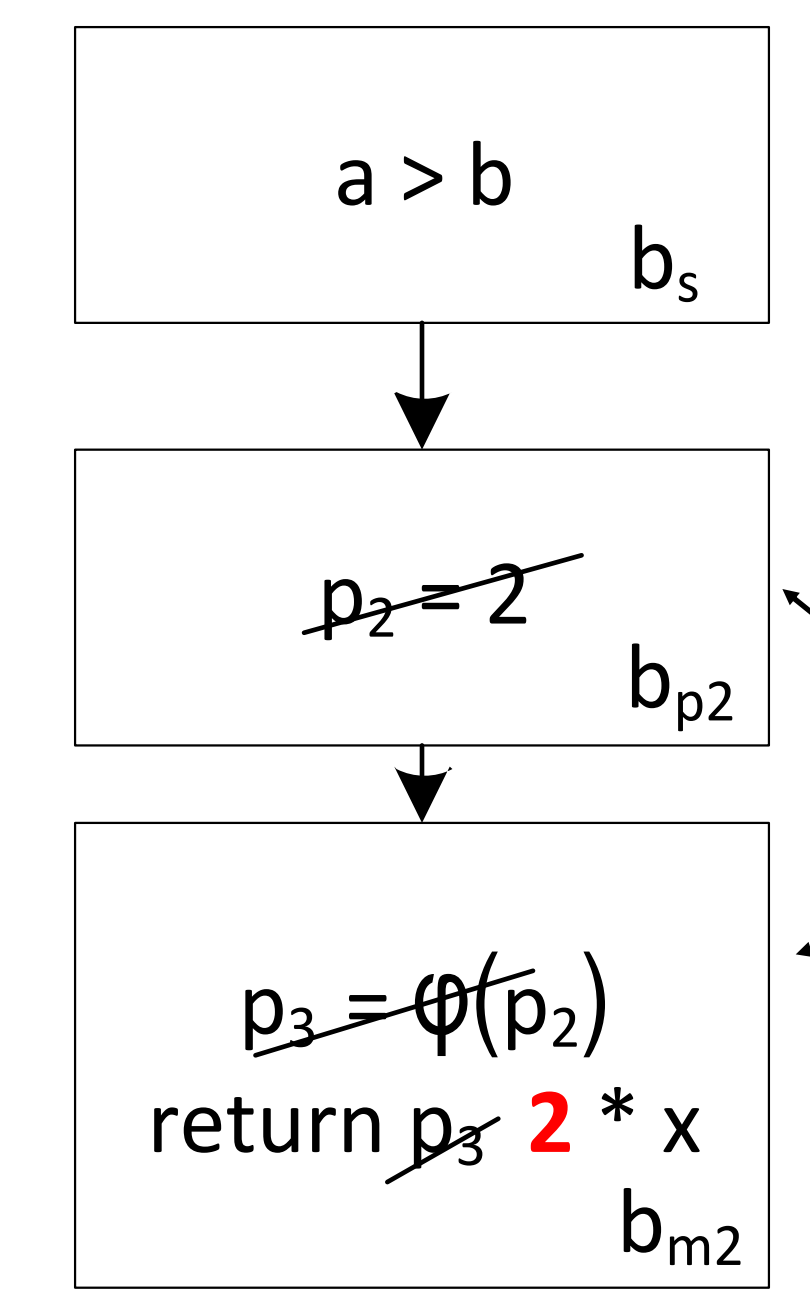
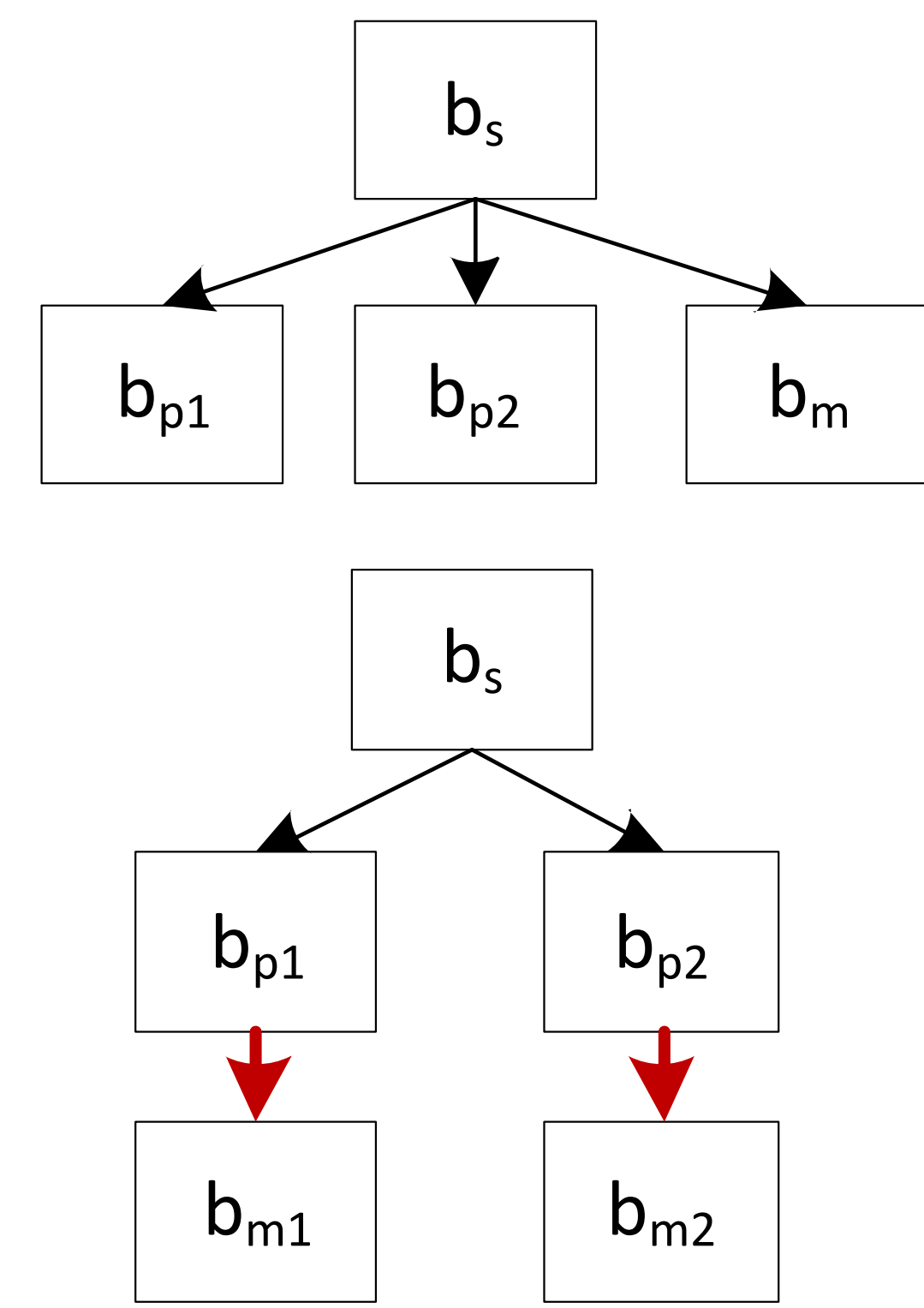
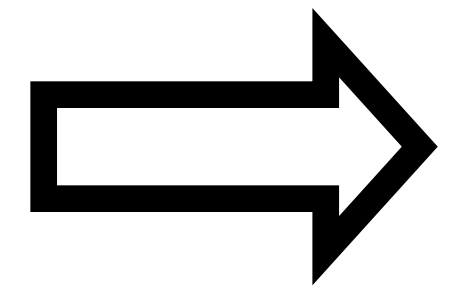
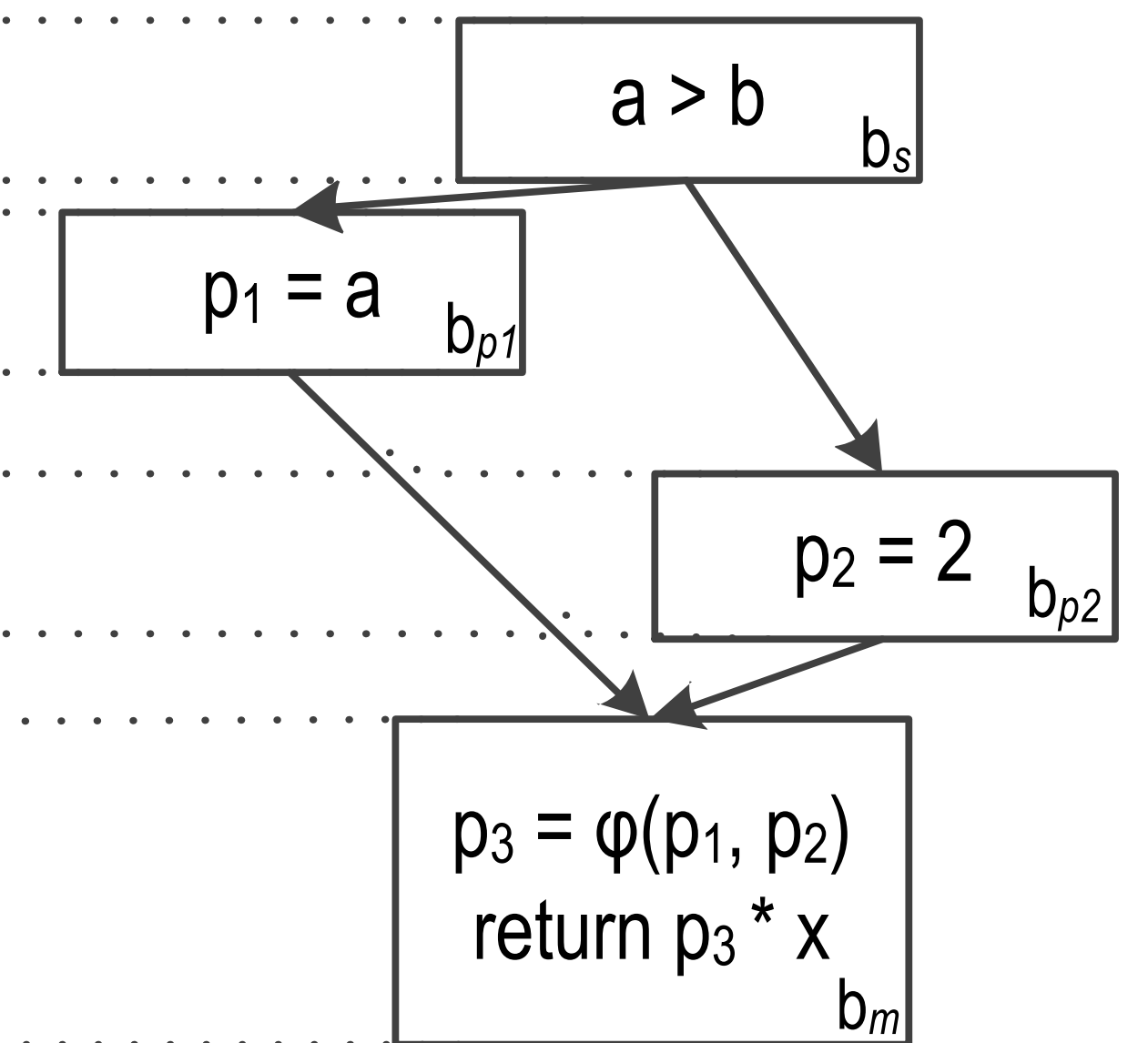


Problem	Solution: Code Duplication	Cost/Benefit Analysis
<p>⇒ Control-flow prohibits many optimizations</p> <pre> graph TD p1["p1 = a"] p2["p2 = 2"] p3["p3 = φ(p1, p2) use p3 ..."] p1 --> p3 p2 --> p3 </pre>	<p>⇒ Copy merge block into predecessors</p> <p>⇒ Eliminate merges ⇒ Increase code size</p>	<p>⇒ Find opportunities prior to duplication</p> <p>⇒ Duplicate as little as possible</p>

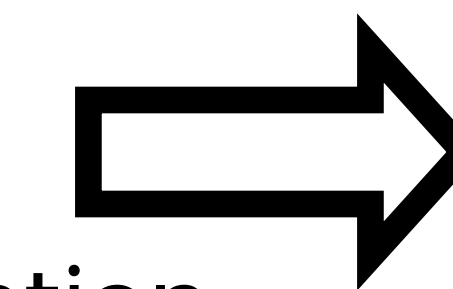
Approach: Simulation-based Code Duplication

```

int f(int a, int b, int x) {
    int p;
    if (a > b) {
        p = a;
    } else {
        p = 2;
    }
    return p * x;
}
    
```



Benefit
Copy Propagation
Strength Reduction

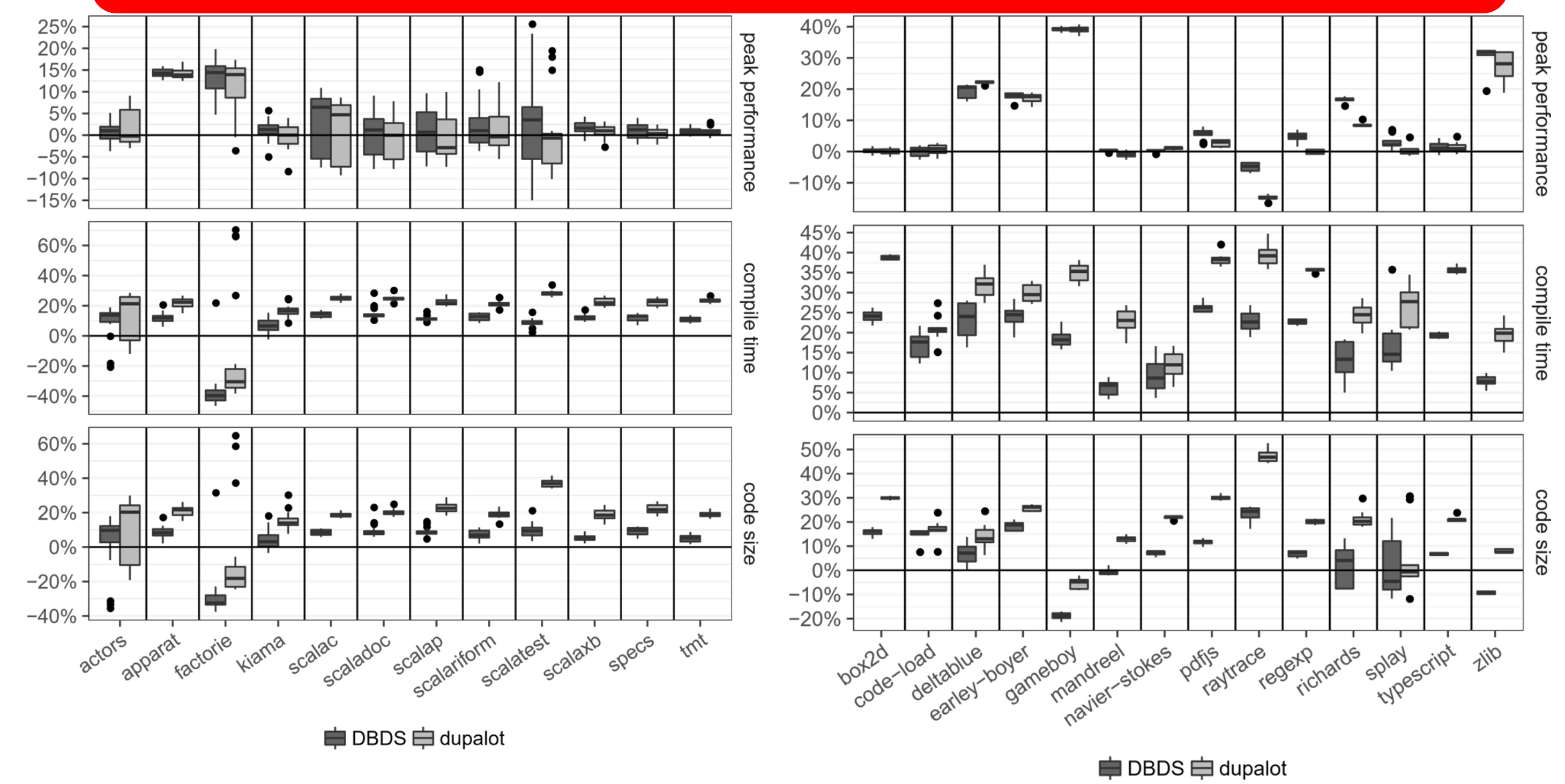


```

int f(int a, int b, int x) {
    if (a > b) {
        return a * x;
    } else {
        return x << 1;
    }
}
    
```

Cost

Performance



- Simulate** Duplication
- Find Optimization **Opportunities & Trade-Off**
 - ⇒ **Quantify** Opportunities
 - ⇒ **Cost / Benefit Analysis:** Code Size vs Peak
- Duplicate & **Optimize**

Why simulate ?

- ⇒ No backtracking
- ⇒ Make compile-time overhead feasible
- ⇒ No cleanup necessary

Try it out now !



graalvm.github.io